

Layout and Schematic Translators V4.0 Readme File

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This Readme file contains the latest information for installing and using programs contained within the PADS Translators V4.0 release.

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Installation and licensing notes

General information

You can find translation instructions, including limitations, in the translator help files, which are available from the Help buttons in the Translators or in the C:\MentorGraphics\2005TPD\docs folder.

Advisories

Advisories for the Translators will be available on the Web in the near future.

System requirements

This release requires the following:

- Windows XP Professional or Home Edition (service pack 1 or service pack 2), or Windows 2000 (service pack 4)

Installation preparation

Remove any existing versions

If you have any other versions of these translators on your system, remove them before installing this new version.

Important: If you have made modifications to the Schematic Translator's attribute mapping (.cnv) files, back up these files before removing the old release. After the new release is installed, copy the files back to the Schematic Translator directory, then use the Translator's Mapping tab to select the mapping file you want to use.

Administrator privileges

You must have Administrator privileges to install PADS Translators.

Disable antivirus software and screen savers

Disable virus shields and screen savers before installing the translators.

Installation

Installation instructions

Follow the screen prompts during installation for instructions.

Licensing

The PADS Translators are not secured.

Running the applications

Power User privileges to run the programs

Users must have Power User privileges to run the PADS translators.

PADS Translators

For general information, supported file types, and known issues and limitations, see the Help files. You can access the help systems by clicking Help in the translator dialog boxes.

To start the translators and access help:

1. On the Start menu, point to
Programs > Mentor Graphics SDD > Translators > PADS
and then click either **Layout** or **Schematic**.
2. Do one of the following:
 - For the Layout Translator:
 1. Click **Help**.
 - For the Schematic Translator:
 1. In the Source list, select type of files to translate: **OrCAD Capture**, **Protel 99SE/DXP**, **PADS Logic**, **P-CAD**, or **CADSTAR**.
 2. In the Output list, select the file format to output: **PADS Logic** or **DxDesigner**.
3. Click **OK**, and then click **Help**.

Corrected Problems

Layout Translator

PADSTrans_Orcad_to_PADSLayout

DR 396922— Translator change pin numbers

DR 399895— Orcad library translator

PADSTrans_Pcad_to_PADSLayout

- DR 396297**— Very large number of clearance errors showing up in translated PCAD design.
- DR 313446**— PCAD File cannot be translated. Gets internal error and fails.
- DR 314236** — Unable to Translate the attached PCad PCB file in to PADS.
- DR 314562** — Layout Translator fails to start if PADS2004 SPac2 was last opened.
- DR 348990** — DEF: PCAD to PADS Layout translation produces two or more Decal entries.
- DR 351649** — PADS Layout will closed automatically will loading a PCB file translated from P-CAD
- DR 362718** — Traces unrouted during translation from PCAD to PADS?

PADSTrans_Protel_to_PADSLayout

- DR 344324**— Protel file translation ends with Internal Program Error after long wait.
- DR 346572**— Unable to translate attached Protel pcb file into PADS Layout. Translator hangs during translation.
- DR 359887**— Outer ring pad is dropped from converted Altium design file.
- DR 384336**— Provide control for mapping Protel pin names to pin numbers in Layout.

PADSTrans_Logic_to_DxDesigner

- DR 364413** — Logic > Dx translator - Signal pins being translated to another net name.
- DR 379921**— The Pin labels of some converted symbols are not displayed in library translating.
- DR 197470**— translator search order is not specified
- DR 202041**— Power logic translation error
- DR 311667**— Power Logic to DxDesigner issue
- DR 314808**— When translating schematics from PADSLogic to DxDesigner.
- DR 318709**— Schematic translator hangs on Logic sch file.
- DR 337485**— Pin numbering not preserved on translated part.
- DR 344723**— PADS Logic schematic causes translator to crash.
- DR 351512**— Pin Names are remapped during translation and should not be.

- DR 361992**— The pin label assignment are incorrect after translating from PADSLogic to DxDesigner.
- DR 367885**— Pin attribute (PinName) is not translated correctly.
- DR 372139**— "Translation of attached PADS Logic design leaves parens around REFDES, causing data loss when Tools, Package CDB runs".
- DR 383576**— PADS Logic schematic sheet order is re-arranged during translation.
- DR 389266**— Can not be translated a symbol in a particular case on Pads Logic to DxDesigner Translator.

PADSTrans_Orcad_to_DxDesigner

- DR 370662** — Many errors in translation and Page 8 of translated OrCAD schematic file crashes in DX.
- DR 375236** — Orcad Translator to DXDesigner not translating a schematic symbol correctly.
- DR 396651** — "Orcad to Dx Translator mapping to too small font, change in behavior."
- DR 400086** — Loops are completely deleted after the translation.
- DR 379473** — The translated design from ORCAD causes DxDesigner to crash.
- DR 300487** — rename of net aliasing is inconsistent.
- DR 317343** — "Getting lots of 6074, 6053 error while netlisting DxDesigner file, which is translated from OrCAD".
- DR 325035** — AltoExp "Orcad to DxDesigner schematic and symbol translators 2.0, 3.0 ignore PIN TYPES mapping".
- DR 329103** — AltoExp Orcad to DxDesigner Translator: Some how It did not translate net name attributes.
- DR 334676** — Translator from Orcad to DxDesigner does not translate library parts correctly.
- DR 334677** — Orcad schematic Translator is not passing the content of the attribute called Value!
- DR 334678** — Attributes are not being placed correctly during library conversion from Orcad to DxDesigner.
- DR 334680** — Round Pin Number visibility is incorrect after converting library parts from Orcad to DxDesigner.
- DR 345970** — Orcad/Protel -> DxDesigner Schematic translator omits buses and rippers.

PADSTrans_Orcad_to_PADSLogic

- DR 279226** — ENH - Allow specific PCB decal mapping for OrCAD to PowerLogic.
- DR 310943** — "Pin swap class is set to -1, though only values >=0 are permitted".
- DR 315554** — OrCAD schematic file gets Internal error when trying to convert to Logic or DxD.
- DR 318191** — Files causing fatal internal errors when translating OrCAD to PADS Logic.
- DR 320705**— DEF: .DSN will not translate with v3.0.116, internal program error
- DR 359016**— Elements of schematic are not on grid.

PADSTrans_Pcad_to_DxDesigner

- DR 341521** — Round Attributes not being translated from PCAD to DxDesigner.
- DR 342530** — blocksize of translated symbols is too small.

PADSTrans_Pcad_to_PADSLogic

- DR 345983**— Translated PCAD to Logic file crashes during loading.
- DR 347923**— DEF: After translation Pin name, Net w/ offpage references are mis-aligned.
- DR 348241** — PCAD to Logic translated file has a number of problems with bus.
- DR 387376**— PCAD to PADS Logic Translator - File crashes.

PADSTrans_Protel_to_DxDesigner

- DR 405628**— Protel to DxDesigner produces an ""(F) Internal Program Error"".
- DR 314774**— Translator issues from protel.
- DR 383383**— Modify Protel to DxDesigner translator to recognize additional field in position entry window.

PADSTrans_Protel_to_PADSLogic

- DR 405627** — Protel Translation to Logic generates ""Internal Program Error"".
- DR 354610** — Translator is translating sheet by sheet instead of entire project.

Support Information

If you have questions about this software release, please log in to SupportNet. You may search the KnowledgeBase with thousands of technical solutions or open a Service Request online at:

<http://www.mentor.com/supportnet>

If you do not have a SupportNet login, you may easily request one by filling out the short form:

<http://www.mentor.com/supportnet/quickaccess/SelfReg.do>

All customer support contact information can be found on our web site at:

http://www.mentor.com/supportnet/support_offices.html